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**Developer Kit D2000 | Bench Talk**  
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Enabling IoT OSs for Intel Quark MCU  
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Intel Announces Wearable Technology and the Quark Processor *Intel Baytrail SOC Explained Intel News: Bay Trail, Broadwell, \u0026 Quark!* Intel Quark D2000 Blink LED Test Internet of Things (IoT) - Perspectives from Quark ~~IDF 2015 Quark BMS Demo~~ Stefano Zambon (Elk) - Turn Software

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## Plugins into Hardware Instruments

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Intel Quark, CPU compatible con  
Arduino - Maker Faire Roma The Intel  
Quark Soc

Intel Quark is a line of 32-bit x86 SoCs  
and microcontrollers by Intel, designed  
for small size and low power  
consumption, and targeted at new

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markets including wearable devices. The line was introduced at Intel Developer Forum in 2013. Quark processors, while slower than Atom processors, are much smaller

Intel Quark - Wikipedia

Intel® Quark™ SoC product listing with

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links to detailed product features and specifications.

Intel® Quark™ SoC Product Specifications

Intel® Quark™ SoC. Support information for Intel® Quark™ SoC related to product highlights, featured

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Formerly Clanton. The Intel® Quark™  
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secure SoC, designed to bring intelligence to the network edge and reduce development costs for securely managed Internet of Things (IoT) endpoint devices.

Intel® Quark™ SoC X1000  
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Support for Intel® Quark™ SoC  
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Intel's 32-bit Quark SoCs and microcontrollers are aimed at IoT applications, including wearables, smart home devices, industrial, and other. Intel's customers will have to make their final Quark...

Intel Discontinues Quark SoCs and

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Microcontrollers

Temperature sensor (on Intel® Quark™ processor core) 2 USB ports (JTAG debug and direct SoC) Integrated development environment provided by Intel® System Studio for Microcontrollers, which is an Eclipse\*-based IDE for developing,

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optimizing and debugging applications;  
Sample applications and  
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Intel® Quark™ SE Microcontroller  
C1000: Features

Intel® Quark™ Microcontroller D2000 :  
Ultra-low-power 32-bit Intel®

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architecture device that integrates an Intel® Quark™ processor core, memory subsystem with on-die volatile and non-volatile storage, and I/O interfaces into a single low-cost system-on-chip solution : Formerly Mint Valley: 32 MHz. 32 KB instruction 8 KB OTP 4 KB data: 8 KB SRAM. Q3'15

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Intel® Quark™ Microcontrollers

The Intel® Quark™ SoC X1000 series brings intelligence to the network edge and reduces costs for securely managed Internet of Things endpoint devices.

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The Intel® Quark™ SoC X1000 is Intel's lowest-power secure SoC, designed to bring intelligence to the network edge and reduce development costs for securely managed Internet of Things (IoT)

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endpoint devices.

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Quark™ SoC X1000 Board Support Package (BSP) Build and Software User Guide, is divided into two major sections: Part 1 Building the BSP Software contains instructions for installing and

Intel Quark SoC X1000

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The Intel® Quark™ SE microcontroller C1000 has learning mechanism technology, allowing the microcontroller to learn and differentiate information.

Intel® Quark™ SE Microcontroller  
C1000: Documentation

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Tanya Pelletier, an Intel® Quark™ SoC Market Development Manager, discusses the SoC's market sectors and segment opportunities in entry-level, edge applications, and the selection criteria for using the Intel® Quark™ SoC.

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